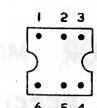
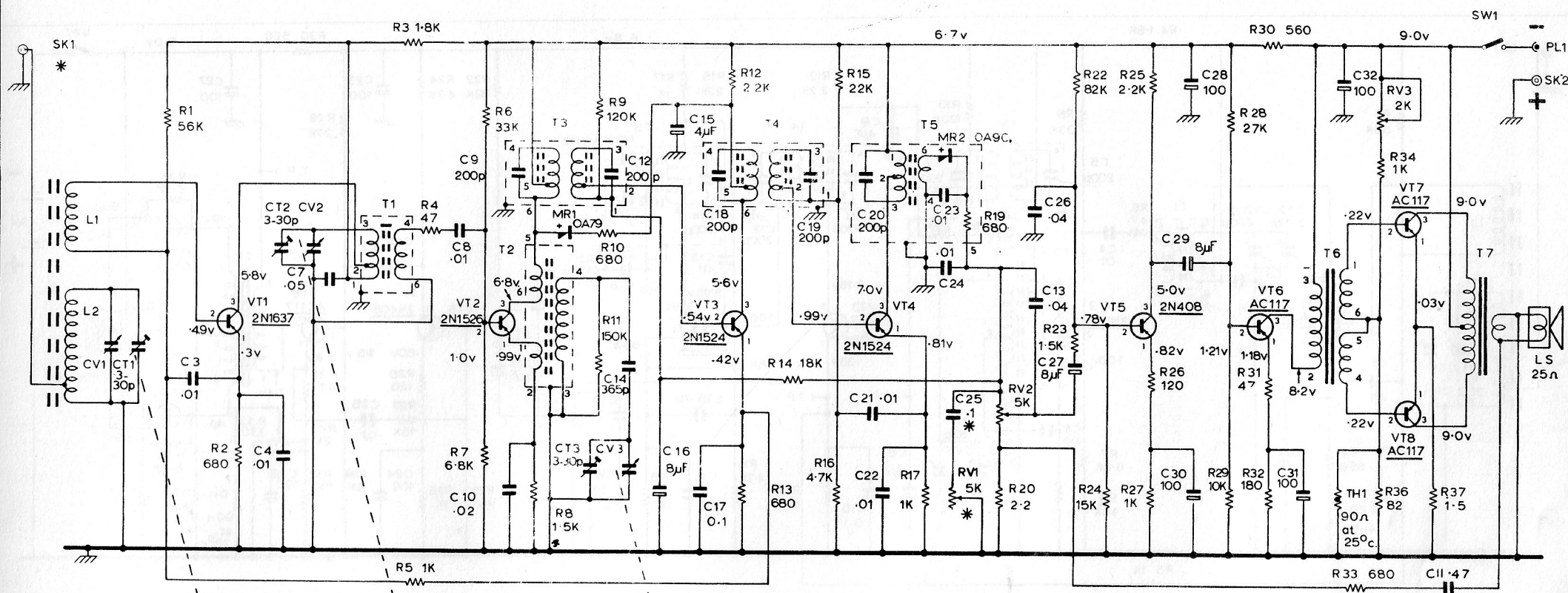
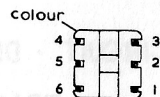


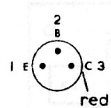
CAPACITORS.			3	4	7	8	9	10	12	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	36	37	II							
RESISTORS			1	2		3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	36	37	II
MISC			SK1	L1	L2	CV1	CT1	VT1	CT2	CV2	T1	VT2	T2	T3	MR1	CT3	CV3	VT3	T4	VT4	T5	MR2	RV1	RV2	VT5	VT6	T6	TH1	RV3	VT7	VT8	SW1	T7	SK2	PL1	L'S				



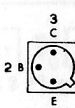
T1-T5 BASE
VIEWED ON
PINS



DRIVER TRANSFORMER
VIEWED FROM TOP



BASES OF TRANSISTORS.



AC117

1. ALL VALUES OF RESISTANCE IN OHMS & ALL VALUES OF CAPACITANCE IN MICRO-FARADS UNLESS OTHERWISE STATED.
2. VOLTAGES STATED ARE NEGATIVE WITH RESPECT TO CHASSIS & MEASURED WITH AVO NO.8 UNDER NO SIGNAL CONDITIONS & WITH VOLUME CONTROL SET TO ZERO.
3. * OMITTED FROM MODEL P222.

CIRCUIT DIAGRAM FOR MURPHY MODEL P87 TRANSISTOR RECEIVER

ISSUES	DATE	BY	REVISION
1	1.3.7.64		

DRAWN	R	CHK'D	APPRVD	RE	used on
DATE	9.6.64	DATE	23.7.64	DATE	23.7.64
CIRCUIT DIAGRAM - P87 -					CP 4853
ALLIED INDUSTRIES LIMITED AUCKLAND N.Z.					